JAN 1 8 2006

Docket No.: P2001,0349

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Ву:

Date: January 14, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No.

10/717,337

Confirmation No: 8258

Applicant

Alexander Benedix, et al.

Filed

November 19, 2003

Title

Method for Determining the Optimum Access Strategy

Art Unit

2186

Examiner

Samuel A. Dillon

Docket No.

P2001,0349

Customer No. :

24131

<u>UNDER 37 C.F.R. 1.97(C)(2)</u>

Hon. Commissioner for Patents Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are cited herewith:

MoSys Incorporated: "Application Note - Memory Controller Design - Using MoSys High-Speed SGRAM", AN-SG01 Rev. 1.4 - 9/1/1998, Sunnyvale, CA, pp. 1-14.

In accordance with 37 C.F.R. 1.97 (c) (2), consideration of this Information Disclosure Statement is requested.

Enclosed is the fee in the amount of \$180.00.

Respectfully submitted,

For Applicant

Date: January 11, 2006

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Tel: (954) 925-1100 Fax: (954) 925-1101

/am

Alfred K. Dassler 52,794

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U.S. DEPARTI									
PATENT AND					Applicant Alexander Benedix, et al.				
SIA					Filing Date November 19, 2003		Group Art Unit 2186		
		U.S. I	PATENT	DOCUMENTS					
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.